

X20ATA312

Data sheet
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Version history

B&R makes every effort to keep documents as current as possible. The most current versions are available for download on the B&R website (www.br-automation.com).

1 General information

1.1 Other applicable documents

For additional and supplementary information, see the following documents.

Other applicable documents

Document name	Title
MAX20	X20 System user's manual

1.2 Order data

Order number	Short description	Figure
	Temperature measurement	
X20ATA312	X20 temperature input module, 2 resistance measurement inputs, Pt100, resolution 0.01°C, 4-wire connections, Net-Time function	
	Required accessories	
	Bus modules	
X20BM11	X20 bus module, 24 VDC keyed, internal I/O power supply connected through	
X20BM15	X20 bus module, with node number switch, 24 VDC keyed, internal I/O power supply connected through	
	Terminal blocks	
X20TB1F	X20 terminal block, 16-pin, 24 VDC keyed	

Table 1: X20ATA312 - Order data

1.3 Module description

The module is equipped with 2 inputs for PT100 4-line resistance temperature measurement.

Functions:

- [Sensor type and measurement range](#)
- [Configurable conversion rate / filter time](#)
- [Monitoring the input signal](#)
- [NetTime Technology](#)

Sensor type and measurement range

The module can be used for both measurement sensor and resistance measurement. The measurement range varies depending on the operating mode set.

Conversion rate and filter time

The sampling time of the A/D converter can be configured together with the filter time.

Monitoring the input signal

The input signal is monitored against the upper and lower limit values as well as for open circuit. In addition to the status information, user-defined limit values can be defined as well as replacement values that are output if the limit values are overshoot or undershot.

NetTime timestamp of the measurement

For many applications, not only the measured value is important, but also the exact time of the measurement. The module is equipped with a NetTime timestamp function for this that supplies a timestamp for the recorded position and trigger time with microsecond accuracy.

2 Technical description

2.1 Technical data

Order number	X20ATA312
Short description	
I/O module	2 inputs for Pt100 resistance temperature measurement
General information	
B&R ID code	0xE0E4
Status indicators	I/O function per channel, operating state, module status
Diagnostics	
Module run/error	Yes, using LED status indicator and software
Inputs	Yes, using LED status indicator and software
Power consumption	
Bus	0.01 W
Internal I/O	0.4 W
Additional power dissipation caused by actuators (resistive) [W]	-
Certifications	
CE	Yes
UKCA	Yes
ATEX	Zone 2, II 3G Ex nA nC IIA T5 Gc IP20, Ta (see X20 user's manual) FTZÜ 09 ATEX 0083X
UL	cULus E115267 Industrial control equipment
HazLoc	cCSAus 244665 Process control equipment for hazardous locations Class I, Division 2, Groups ABCD, T5
DNV	Temperature: B (0 to 55°C) Humidity: B (up to 100%) Vibration: B (4 g) EMC: B (bridge and open deck)
CCS	Yes
LR	ENV1
ABS	Yes
BV	EC33B Temperature: 5 - 55°C Vibration: 4 g EMC: Bridge and open deck
Resistance measurement temperature inputs	
Input	Resistance measurement with constant current supply for 4-wire connections
Digital converter resolution	24-bit
Filter time	Configurable between 1 and 200 ms
Conversion time	
1 channel	20 ms with 50 Hz filter
2 channels	40 ms per channel with 50 Hz filter
Conversion procedure	Sigma-delta
Output format	DINT or UDINT for resistance measurement
Temperature measurement range	-200 to 850°C
Resistance measurement range	0.5 to 390 Ω
Temperature sensor resolution	1 LSB = 0.01°C
Resistance measurement resolution	0.001 Ω
Input filter	First-order low-pass filter / cutoff frequency 1050 Hz
Sensor standard	EN 60751
Insulation voltage between channel and bus	500 V _{eff}
Linearization method	Internal
Measurement current	1 mA
Temperature sensor normalization	-200.0 to 850.0°C
Reference	1568 Ω ±0.1%
Permissible input signal	Short-term max. 28.8 V
Max. error at 25°C ¹⁾	
Gain	±0.0059% ²⁾
Offset	±0.0015% ³⁾
Max. gain drift	±0.00065%/°C ²⁾
Max. offset drift	±0.000025%/°C ³⁾
Nonlinearity	<0.001% ³⁾

Table 2: X20ATA312 - Technical data

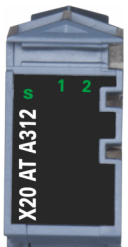
Order number	X20ATA312
Standardized range of values for resistance measurement	19 to 390 Ω
Temperature measurement monitoring	
Range undershoot	0x80000001
Range overshoot	0x7FFFFFFF
Open circuit	0x7FFFFFFF
General fault	0x80000000
Open inputs	0x7FFFFFFF
Resistance measurement monitoring	
Range undershoot	0x80000001
Range overshoot	0xFFFFFFFF
Open circuit	0xFFFFFFFF
General fault	0x80000000
Electrical properties	
Electrical isolation	Channel isolated from bus Channel not isolated from channel
Operating conditions	
Mounting orientation	
Horizontal	Yes
Vertical	Yes
Installation elevation above sea level	
0 to 2000 m	No limitation
>2000 m	Reduction of ambient temperature by 0.5°C per 100 m
Degree of protection per EN 60529	IP20
Ambient conditions	
Temperature	
Operation	
Horizontal mounting orientation	-25 to 60°C
Vertical mounting orientation	-25 to 50°C
Derating	-
Storage	-40 to 85°C
Transport	-40 to 85°C
Relative humidity	
Operation	5 to 95%, non-condensing
Storage	5 to 95%, non-condensing
Transport	5 to 95%, non-condensing
Mechanical properties	
Note	Order 1x terminal block X20TB1F separately. Order 1x bus module X20BM11 separately.
Pitch	12.5 ^{+0.2} mm

Table 2: X20ATA312 - Technical data

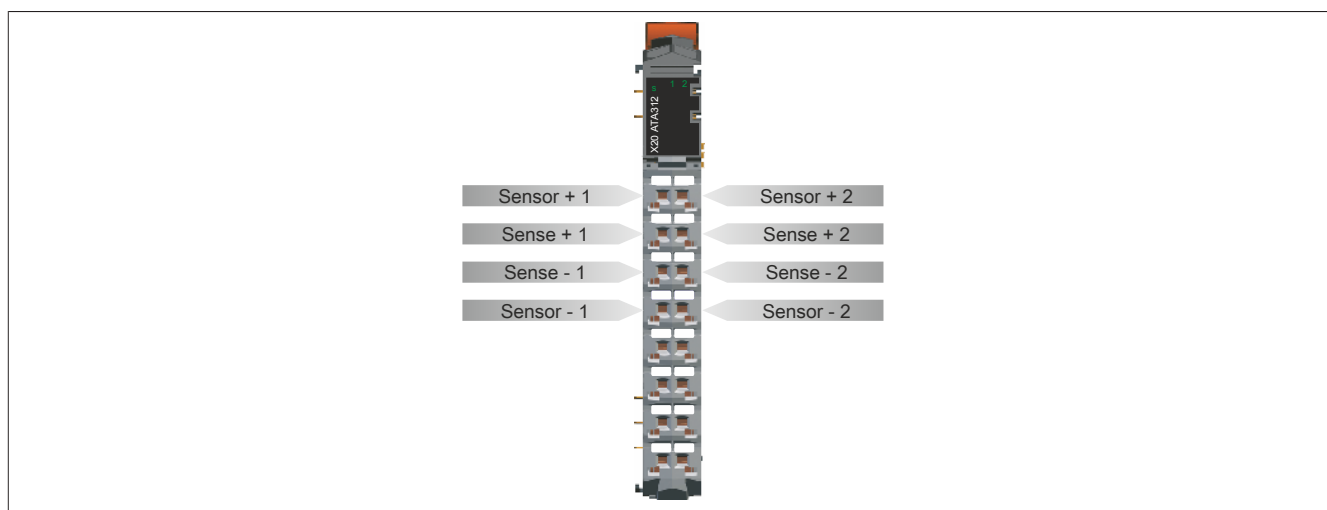
- 1) To guarantee accuracy, modules with a power dissipation < 1.2 W must be connected to the left and right of this module.
- 2) Based on the current measured resistance value.
- 3) Based on the entire resistance measurement range.

2.2 Status LEDs

For a description of the various operating modes, see section "Additional information - Diagnostic LEDs" in the X20 System user's manual.

Image	LED	Color	Status	Description
	s	Green	Off	Module supply not connected
			Single flash	Reset mode
			Double flash	Mode BOOT (during firmware update) ¹⁾
			Blinking	PREOPERATIONAL mode
			On	RUN mode
		Red	Off	Module supply not connected or everything OK
			On	Error or reset status
	1 - 2	Green	Single flash	Parameter or conversion error ²⁾
			Red on / Green single flash	Invalid firmware
			Off	Input turned off or not supplied
			Single flash	Parameter error ²⁾
			Double flash	Conversion error ²⁾
		Blinking		Overflow, underflow or open line
			On	A/D converter running, value OK

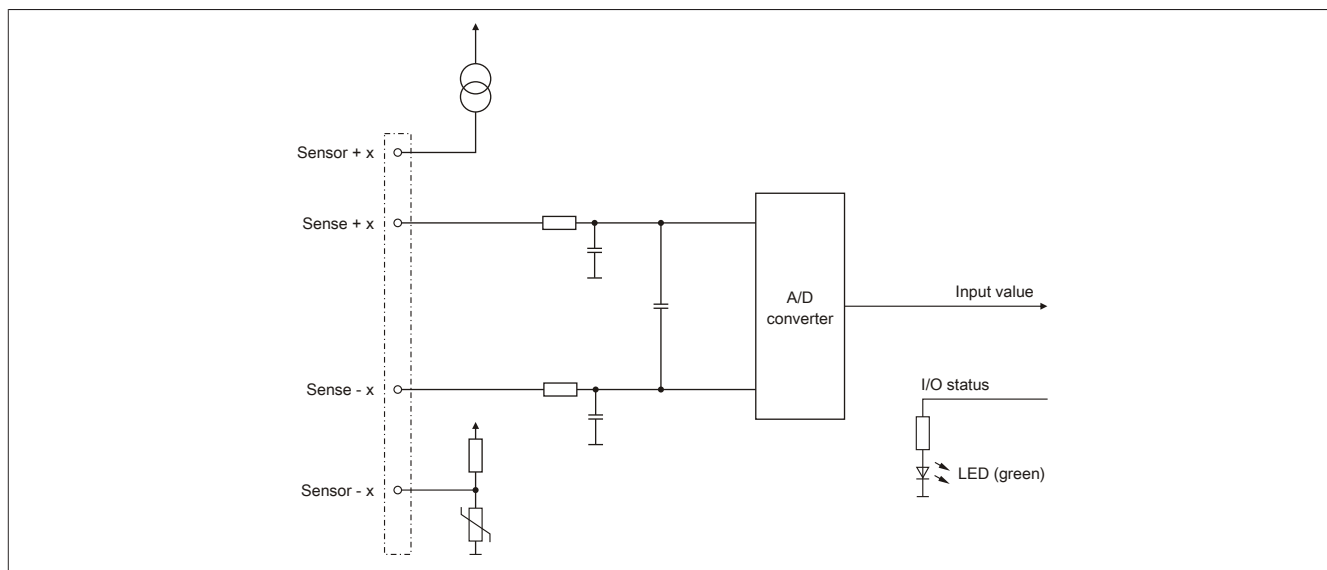
- 1) Depending on the configuration, a firmware update can take up to several minutes.
- 2) Parameter or converter errors are indicated simultaneously on the red "s" LED and the channel LED of the respective output.



The diagram shows a 16-channel relay module with a 16-pin header. The header pins are arranged in two rows of eight. The top row of eight pins is connected to the common terminals of 8 relays, labeled 'Sensor 1' through 'Sensor 8'. The bottom row of eight pins is connected to the common terminals of another set of 8 relays, labeled 'Sensor 9' through 'Sensor 16'. Each relay has a switch symbol indicating its state. The module is powered by a +24 VDC supply and GND, connected to the bottom two pins of the header. A label 'AT' is present on the module.

Any X20 module
X20 module **
Power dissipation < 1.2 W
X20ATx312
X20 module *
Power dissipation < 1.2 W
Any X20 module

2.5 Input circuit diagram



3 Function description

3.1 Sensor type and measurement range

The module can be used for both temperature and resistance measurement. The following measurement ranges result depending on the set operating mode:

Input signal	Measurement range
Pt100 sensor type	10 mK/bit - Temperature measurement
Pt100 sensor type	1 mΩ/bit - Resistance measurement



Information:

The register is described in ["General channel configuration" on page 17.](#)

3.2 Configurable conversion rate / filter time

The sampling time of the A/D converter is configured together with the filter time. The set filter/sampling time applies equally to the inputs of the thermocouples and temperature resistor.

Datentyp	Werte	Filterzeit in ms	Wandelrate in s ⁻¹
UINT	4	1	1000
	9	2	500
	48	10	100
	80	16,7	60
	96	20 (Bus Controller Default)	50
	160	33,3	30
	192	40	25
	320	66,7	15
	480	100	10
	960	200	5



Information:

Je geringer die Wandelrate konfiguriert wird, desto genauer kann der Wert gewandelt werden. Allerdings wird dadurch auch die I/O-Updatezeit erhöht.



Information:

The register is described in ["Setting the conversion rate" on page 16.](#)

3.3 Monitoring the input signal

The input signal is monitored against the upper and lower limit values as well as for open circuit. In addition to the status information, user-defined limit values can be defined as well as replacement values that are output if the limit values are overshoot or undershot.

The module detects the occurrence of errors and sends them to the application. When using function model Standard (0), the triggering behavior of the status messages can be influenced via register "Delay". In Automation Studio, an error message can be read out either as a whole register or bit by bit.

3.3.1 User-defined limit values

This module provides the user the option to specify user-defined limits. If the valid measurement range is reduced in this way, the behavior of the replacement value strategy is more likely to be applied.

Permissible measurement range

The valid range is derived from the properties of the sensor being used or the hardware and firmware of the respective B&R module. These values cannot be changed by the user.

Permissible range of values

The range of values is always within the permissible measuring range. By setting the upper and lower limit values, the range of values can be adapted to the requirements of the application.

3.3.2 Replacement value strategy

If a measured value is detected that is outside the permitted value range, the behavior of the input register must still remain clearly defined. The module provides the user two different options for this purpose.

Retain last valid value

With this strategy, the determined measured value is stored temporarily for a specific time and written to the input register after a delay. If an invalid measured value is detected, this value and all values that have been stored temporarily are discarded. The last valid input register value is retained. To update the value in the input register, there must be enough valid values stored in the temporary buffer. The number needed is determined by the time period specified in "PreparationInterval0x".

Replace with static value

With this strategy, the measured value is written to the input register without delay. If an invalid value occurs, it is replaced by a static value that has been predefined by the user.



Information:

The registers are described in "[Configuring the limit values](#)" on page 17 and "[Status messages](#)" on page 20.

3.4 NetTime Technology

NetTime refers to the ability to precisely synchronize and transfer system times between individual components of the controller or network (controller, I/O modules, X2X Link, POWERLINK, etc.).

This allows the moment that events occur to be determined system-wide with microsecond precision. Upcoming events can also be executed precisely at a specified moment.



3.4.1 Time information

Various time information is available in the controller or on the network:

- System time (on the PLC, Automation PC, etc.)
- X2X Link time (for each X2X Link network)
- POWERLINK time (for each POWERLINK network)
- Time data points of I/O modules

The NetTime is based on 32-bit counters, which are increased with microsecond resolution. The sign of the time information changes after 35 min, 47 s, 483 ms and 648 μ s; an overflow occurs after 71 min, 34 s, 967 ms and 296 μ s.

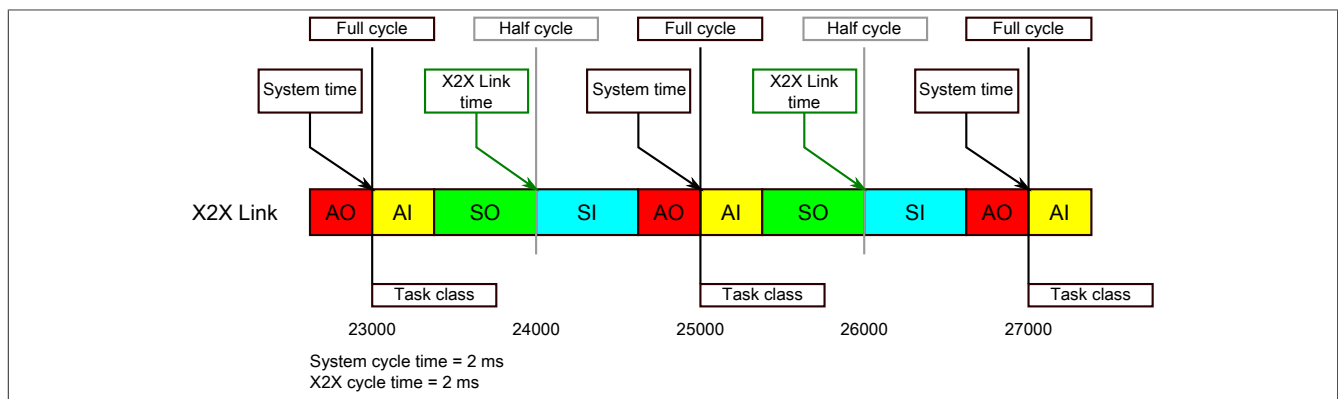
The initialization of the times is based on the system time during the startup of the X2X Link, the I/O modules or the POWERLINK interface.

Current time information in the application can also be determined via library AsIOTime.

3.4.1.1 Controller data points

The NetTime I/O data points of the controller are latched to each system clock and made available.

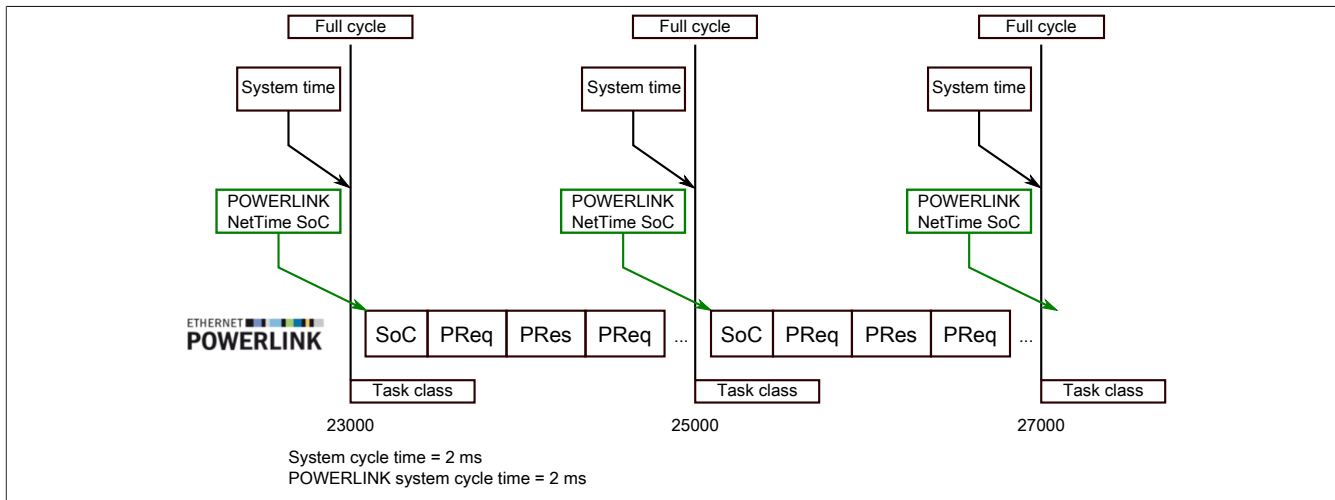
3.4.1.2 X2X Link - Reference time point



The reference time point on the X2X Link network is always calculated at the half cycle of the X2X Link cycle. This results in a difference between the system time and the X2X Link reference time point when the reference time is read out.

In the example above, this results in a difference of 1 ms, i.e. if the system time and X2X Link reference time are compared at time 25000 in the task, then the system time returns the value 25000 and the X2X Link reference time returns the value 24000.

3.4.1.3 POWERLINK - Reference time point

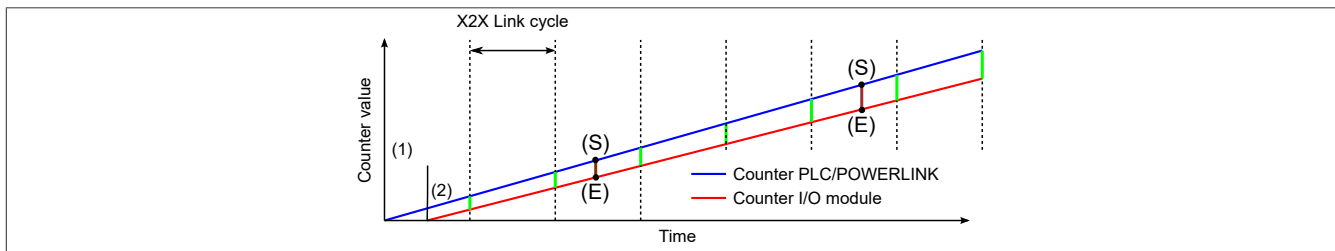


The POWERLINK reference time point is always calculated at the start of cycle (SoC) of the POWERLINK network. The SoC starts 20 μs after the system clock due to the system. This results in the following difference between the system time and the POWERLINK reference time:

POWERLINK reference time = System time - POWERLINK cycle time + 20 μs

In the example above, this means a difference of 1980 μs, i.e. if the system time and POWERLINK reference time are compared at time 25000 in the task, then the system time returns the value 25000 and the POWERLINK reference time returns the value 23020.

3.4.1.4 Synchronization of system time/POWERLINK time and I/O module



At startup, the internal counters for the controller/POWERLINK (1) and the I/O module (2) start at different times and increase the values with microsecond resolution.

At the beginning of each X2X Link cycle, the controller or POWERLINK network sends time information to the I/O module. The I/O module compares this time information with the module's internal time and forms a difference (green line) between the two times and stores it.

When a NetTime event (E) occurs, the internal module time is read out and corrected with the stored difference value (brown line). This means that the exact system moment (S) of an event can always be determined, even if the counters are not absolutely synchronous.

Note

The deviation from the clock signal is strongly exaggerated in the picture as a red line.

3.4.2 Timestamp functions

NetTime-capable modules provide various timestamp functions depending on the scope of functions. If a timestamp event occurs, the module immediately saves the current NetTime. After the respective data is transferred to the controller, including this precise moment, the controller can then evaluate the data using its own NetTime (or system time), if necessary.

For details, see the respective module documentation.

3.4.2.1 Time-based inputs

NetTime Technology can be used to determine the exact moment of a rising edge at an input. The rising and falling edges can also be detected and the duration between 2 events can be determined.



Information:

The determined moment always lies in the past.

3.4.2.2 Time-based outputs

NetTime Technology can be used to specify the exact moment of a rising edge on an output. The rising and falling edges can also be specified and a pulse pattern generated from them.



Information:

The specified time must always be in the future, and the set X2X Link cycle time must be taken into account for the definition of the moment.

3.4.2.3 Time-based measurements

NetTime Technology can be used to determine the exact moment of a measurement that has taken place. Both the starting and end moment of the measurement can be transmitted.

4 Commissioning

4.1 Using the module on the bus controller

Function model 254 "Bus controller" is used by default only by non-configurable bus controllers. All other bus controllers can use other registers and functions depending on the fieldbus used.

For detailed information, see section "Additional information - Using I/O modules on the bus controller" in the X20 user's manual (version 3.50 or later).

4.1.1 CAN I/O bus controller

The module occupies 1 analog logical slot on CAN I/O.

5 Register description

5.1 General data points

In addition to the registers described in the register description, the module has additional general data points. These are not module-specific but contain general information such as serial number and hardware variant.

General data points are described in section "Additional information - General data points" in the X20 System user's manual.

5.2 Function model 0 - Standard

Register	Name	Data type	Read		Write	
			Cyclic	Acyclic	Cyclic	Acyclic
Configuration						
130	InputFilter	UINT				•
134	ModeADC	UINT				•
514	SensorType01	UINT				•
578	SensorType02					
566	PreparationInterval01	UINT				•
630	PreparationInterval02					
548	ReplaceUpper01	DINT				•
612	ReplaceUpper02					
540	ReplaceLower01	DINT				•
604	ReplaceLower02					
532	UpperLimit01	DINT				•
596	UpperLimit02					
524	LowerLimit01	DINT				•
588	LowerLimit02					
554	Hysteresis01	UINT				•
618	Hysteresis02					
558	ErrorDelay01	UINT				•
622	ErrorDelay02					
562	SumErrorDelay01	UINT				•
626	SumErrorDelay02					
Communication						
0	Temperature01	DINT	•			
	Resistor01	UDINT				
4	Temperature02	DINT	•			
	Restistor02	UDINT				
260	Measurand01	DINT		•		
324	Measurand02					
281	IOCycleCounter01	USINT	•			
345	IOCycleCounter02					
282	IOCycleCounter01	UINT	•			
346	IOCycleCounter02					
274	Sampletime01	INT	•			
338	Sampletime02					
276	Sampletime01	DINT	•			
340	Sampletime02					
297	Status01	USINT	•			
	Underrun01	Bit 0				
	Overrun01	Bit 1				
	OpenLine01	Bit 2				
	ConverterFault01	Bit 4				
	SumFault01	Bit 5				
	ParameterFault01	Bit 6				
	IoSupplyFault01	Bit 7				
361	Status02	USINT	•			
	Underrun02	Bit 0				
	Overrun02	Bit 1				
	OpenLine02	Bit 2				
	ConverterFault02	Bit 4				
	SumFault02	Bit 5				
	ParameterFault02	Bit 6				
	IoSupplyFault02	Bit 7				

5.3 Function model 254 - Bus Controller

Register	Offset ¹⁾	Name	Data type	Read		Write	
				Cyclic	Acyclic	Cyclic	Acyclic
Configuration							
130	-	InputFilter	UINT				•
134	-	ModeADC	UINT				•
514	-	SensorType01	UINT				•
578	-	SensorType02	UINT				•
566	-	PreparationInterval01					
630	-	PreparationInterval02					
548	-	ReplaceUpper01	DINT				•
612	-	ReplaceUpper02	DINT				•
540	-	ReplaceLower01					
604	-	ReplaceLower02					
532	-	UpperLimit01	DINT				•
596	-	UpperLimit02	DINT				•
524	-	LowerLimit01					
588	-	LowerLimit02					
554	-	Hysteresis01	UINT				•
618	-	Hysteresis02	UINT				•
558	-	ErrorDelay01					
622	-	ErrorDelay02					
562	-	SumErrorDelay01	UINT				•
626	-	SumErrorDelay02					
Communication							
0	0	Temperature01	DINT	•			
		Resistor01	UDINT				
4	4	Temperature02	DINT	•			
		Restistor02	UDINT				
281	-	IOCycleCounter01	USINT		•		
345	-	IOCycleCounter02					
30	-	Status01To02	USINT		•		

1) The offset specifies where the register is within the CAN object.

5.4 Configuration of the A/D converter

5.4.1 Setting the conversion rate

Name:

InputFilter

Mit Hilfe dieses Registers wird die Abtastzeit des A/D-Wandlers konfiguriert.

Datentyp	Werte	Filterzeit in ms	Wandelrate in s ⁻¹
UINT	4	1	1000
	9	2	500
	48	10	100
	80	16,7	60
	96	20 (Bus Controller Default)	50
	160	33,3	30
	192	40	25
	320	66,7	15
	480	100	10
	960	200	5



Information:

Je geringer die Wandelrate konfiguriert wird, desto genauer kann der Wert gewandelt werden. Allerdings wird dadurch auch die I/O-Updatezeit erhöht.

5.4.2 Operating mode of the A/D converter

Name:

ModeADC

The operating mode for the A/D converter can be configured in this register.

The individual options allow faster digitalization of the analog values, but this also reduces the precision of the measured values.

Data type	Values	Bus controller default setting
UINT	See the bit structure.	0

Bit structure:

Bit	Description	Value	Information
0	Chopper mode	0	Alternating gain of the analog value (bus controller default setting)
		1	Chopper mode off
1	Order of the SINC filter	0	SINC4 (bus controller default setting)
		1	SINC3
2 - 15	Reserved	-	-

The following applies:

$$\begin{aligned} \text{ConversionTime(SINC3)} &= \text{ConversionTime(SINC4)} - 1 \times \text{ConversionCycle} \\ \text{ConversionTime(without Chop)} &= 0.5 \times \text{ConversionTime(Chop)} \end{aligned}$$

5.5 Configuring the measurement channels

Each temperature measurement channel can be configured independently. All registers required for this were configured individually for each channel.

5.5.1 General channel configuration

Name:

SensorType01 to SensorType02

This register defines the basic behavior of the channel.

Data type	Values	Bus controller default setting
UINT	See the bit structure.	129

Bit structure:

Bit	Description	Value	Information
0 - 2	Sensor type with unit and resolution	001	Pt100 [10 mK/bit] - Temperature measurement (bus controller default setting)
		010	Pt100 [1 mΩ/bit] - Resistance measurement
		011 to 111	Reserved
3 - 4	Reserved	-	
5	Replacement value strategy	0	Replacing statically
		1	Retain last valid value
6	Monitoring the user-defined limit values	0	Switch off additional limits
		1	Switch on additional limits
7	Channel (on/off)	0	Switch off the entire channel
		1	Switch on channel (bus controller default setting)
8 - 15	Reserved	-	

5.6 Configuring the limit values

5.6.1 Hysteresis

Name

Hysteresis01 to Hysteresis02

A hysteresis can be defined to avoid frequent state changes in the measurement range close to the limit value. This defines a small section at the edge of the permissible range of values in which the measured values retain the status (permissible or impermissible) of the previous measured value.

Data type	Values	Information
UINT	0 to 65535	Bus controller default setting: 16

5.6.2 Upper limit value

Name:

UpperLimit01 to UpperLimit02

This register specifies the upper limit value. The values entered should be within the valid measurement range.

Data type	Values	Information
DINT	-2,147,483,648 to 2,147,483,647	Bus controller default setting: 2,147,483,647

5.6.3 Lower limit value

Name:

LowerLimit01 to LowerLimit02

The lower limit value is specified in this register. The value entered should be within the permissible measurement range.

Data type	Values	Information
DINT	-2,147,483,648 to 2,147,483,647	Bus controller default setting: -2,147,483,647

5.6.4 Preparation interval

Name:

PreparationInterval01 to PreparationInterval02

This register is used to define the time interval within which the measured value is checked before it is forwarded.

Data type	Value	Information
UINT	0 to 65535	Unit in 0.1 ms. Bus controller default setting: 0



Information:

This register only needs to be defined if replacement value strategy "Retain last valid value" was selected in register ["SensorType0x" on page 17](#).

5.6.5 Static replacement when overshooting the upper limit

Name:

ReplaceUpper01 to ReplaceUpper02

This register is used to specify the replacement value that is output in place of the impermissible measured value if the upper limit value is violated.

Data type	Values	Information
DINT	-2,147,483,648 to 2,147,483,647	Bus controller default setting: 2,147,483,647



Information:

This register must be defined if the replacement value strategy "Replace with static value" was selected in register ["SensorType0x" on page 17](#).

5.6.6 Static replacement when undershooting the lower limit

Name:

ReplaceLower01 to ReplaceLower02

This register is used to specify the replacement value that is output in place of the impermissible measured value if the lower limit value is violated.

Data type	Values	Information
DINT	-2,147,483,648 to 2,147,483,647	Bus controller default setting: -2,147,483,647



Information:

This register must only be defined if replacement value strategy "Replace with static value" has been selected in register ["SensorType0x" on page 17](#).

5.7 Communication

The received temperature data is stored with a [timestamp](#) and, depending on the configuration, is made available under various register names and data types.

5.7.1 Measured value – Temperature

Name:

Temperature01 to Temperature02

If the channel is configured for resistance measurement, the current temperature value is made available in this register.

Data type	Values
DINT	-2,147,483,648 to 2,147,483,647

5.7.2 Measured value – Resistance

Name:

Resistor01 to Resistor02

If the channel is configured for resistance measurement, the current resistance value is made available in this register.

Data type	Values
UDINT	0 to 4,294,967,295

5.7.3 Measured value – Unweighted

Name:

Measurand01 to Measurand02

When using library AsIoAcc, the unweighted measured value can be accessed via this register. This is the measured value located within the permissible measurement range and has not yet been compared with the user-defined limit values.

Data type	Values
DINT	-2,147,483,648 to 2,147,483,647



Information:

If no user-defined limits are configured, the value of this register does not differ from the temperature or resistance value.

5.7.4 Cycle counter

Name:

IOCycleCounter01 to IOCycleCounter02

This register is used to provide a continuous counter for the application that is incremented each time a temperature value is read.

Data type	Value	Information
USINT	0 to 32767	AD conversion.
UINT	0 to 65535	AD conversion.

5.7.5 Sampling time - Timestamp

Name:

Sampletime01 to Sampletime02

This register provides the application with the NetTime at the time of temperature recording.

For additional information about NetTime and timestamps, see "[NetTime Technology](#)" on page 10.

Data type	Value	Information
INT	-32768 to 32767	NetTime timestamp in μ s
DINT	-2,147,483,648 to 2,147,483,647	NetTime timestamp in μ s



Information:

The SDC library requires a 16-bit value for the sampling time. It is therefore also prepared as a 16-bit value.

5.8 Status messages

5.8.1 Delaying error messages

Name:

ErrorDelay01 to ErrorDelay02

To avoid false alarms due to short-term measurement deviations, the status messages can be transmitted to the PLC with a delay. This register determines the number of A/D conversions during which a cause of error must exist before it is transmitted as an error message.

Data type	Values	Information
UINT	0 to 65535	AD conversions. Bus controller default setting: 2

5.8.2 Delaying the sum error message

Name:

SumErrorDelay01 to SumErrorDelay02

This register can be used to set the delay time with which bit 5 in register ["Status0x" on page 20](#) is transferred to the controller independently of the other status messages.

Data type	Values	Information
UINT	0 to 65535	Bus controller default setting: 4000

5.8.3 Status messages for function model 0

Name:

Status01 to Status02

The bits of the register are set if an error has been diagnosed and it persists for longer than the delay time configured in register ["ErrorDelay0x" on page 20](#).

Data type	Values
USINT	See the bit structure.

Bit structure:

Bit	Name	Value	Information
0	Underrun01 to Underrun02	0	No error
		1	Value below the permitted range
1	Overrun01 to Overrun02	0	No error
		1	Value above the permitted range
2	OpenLine01 to OpenLine02	0	No error
		1	Sensor is not connected correctly
3	Reserved	-	
4	ConverterFault01 to ConverterFault02	0	No error
		1	Invalid A/D converter output
5	SumFault01 to SumFault02	0	No error
		1	Composite error
6	ParameterFault01 to ParameterFault02	0	No error
		1	The "SensorType0x" on page 17 register is faulty
7	IoSupplyFault01 to IoSupplyFault02	0	No error
		1	The I/O supply voltage is faulty

5.8.4 Status messages for function model 254

Name:
Status01To02

The bits in this register are set if an error has been detected.

Data type	Values
USINT	See the bit structure.

Bit structure:

Bit	Name	Value	Information
0	Underrun on channel 01	0	No error
		1	Value below the permitted range
1	Overrun on channel 01	0	No error
		1	Value above the permitted range
2	Underrun on channel 02	0	No error
		1	Value below the permitted range
3	Overrun on channel 02	0	No error
		1	Value above the permitted range
4 - 7	Reserved	-	



Information:

If an open line is detected on a channel, then both error messages will be displayed at the same time.

5.9 Minimum cycle time

The minimum cycle time defines how far the bus cycle can be reduced without causing a communication error or impaired functionality. It should be noted that very fast cycles decrease the idle time available for handling monitoring, diagnostics and acyclic commands.

Minimum cycle time
200 µs

5.10 Minimum I/O update time

The minimum I/O update time defines how far the bus cycle can be reduced while still allowing an I/O update to take place in each cycle.

Minimum I/O update time
1 ms