X20AI8221

Data sheet
1.30 (June 2025)

1346724721879-1.30

1 General information

1.1 Other applicable documents

For additional and supplementary information, see the following documents.

Other applicable documents

Document name	Title
MAX20	X20 System user's manual

1.2 Order data

Order number	Short description	Figure
	Analog input modules	
X20AI8221	X20 analog input module, 8 inputs, ±10 V, 13-bit converter resolution	
	Required accessories	
	Bus modules	
X20BM11	X20 bus module, 24 VDC keyed, internal I/O power supply connected through	
X20BM15	X20 bus module, with node number switch, 24 VDC keyed, internal I/O power supply connected through	
	Terminal blocks	
X20TB1F	X20 terminal block, 16-pin, 24 VDC keyed	

Table 1: X20AI8221 - Order data

1.3 Module description

The module is equipped with 8 inputs with 13-bit (including sign) digital converter resolution. Voltage signals in the range of ±10 V can be recorded.

Functions:

- Input filter
- Monitoring the input signal

Analog input filter

The module is equipped with a configurable input filter with input ramp limiting.

Monitoring the input signal

The input signal of the analog inputs is monitored against the upper and lower limit values as well as for open circuit.

2 Technical description

2.1 Technical data

Order number	X20AI8221
Short description	
I/O module	8 analog inputs ±10 V
General information	
B&R ID code	0xD82F
Status indicators	I/O function per channel, operating state, module status
Diagnostics	
Module run/error	Yes, using LED status indicator and software
Inputs	Yes, using LED status indicator and software
Power consumption	
Bus	0.01 W
Internal I/O	1.04 W [1]
Additional power dissipation caused by actuators (resistive) [W]	-
Certifications	
CE	Yes
UKCA	Yes
ATEX	Zone 2, II 3G Ex nA nC IIA T5 Gc IP20, Ta (see X20 user's manual) FTZÚ 09 ATEX 0083X
UL	cULus E115267 Industrial control equipment
HazLoc	cCSAus 244665 Process control equipment for hazardous locations Class I, Division 2, Groups ABCD, T5
DNV	Temperature: **B** (0 to 55°C) Humidity: **B** (up to 100%) Vibration: **B** (4 g) EMC: **B** (bridge and open deck)
CCS	Yes
LR	ENV1
KR	Yes
ABS	Yes
BV	**EC33B** Temperature: 5 - 55°C Vibration: 4 g EMC: Bridge and open deck
Analog inputs	
Input	±10 V
Input type	Differential input
Digital converter resolution	±12-bit
Conversion time	1 ms for all inputs
Output format	
Data type	INT
Voltage	INT 0x8001 - 0x7FFF / 1 LSB = 0x0008 = 2.441 mV
Input impedance in signal range	20 MΩ
Input protection	Protection against wiring with supply voltage
Open-circuit detection	Yes, using software
Reverse polarity protection	Yes
Permissible input signal	Max. ±30 V
Output of digital value during overload	Configurable
Conversion procedure	SAR
Input filter	Third-order low-pass filter / Cutoff frequency 1 kHz
Max. error	
Gain	0.08% [2]
Offset	0.015% [3]
Max. gain drift	0.006%/°C [2]
Max. offset drift	0.002%/°C [3]
Common-mode rejection	
DC	70 dB
50 Hz	70 dB
Common-mode range	±12 V
Crosstalk between channels	-70 dB
Nonlinearity	<0.025% [3]

Table 2: X20AI8221 - Technical data

Order number	X20AI8221
Insulation voltage between channel and bus	500 VDC, 1 min
Electrical properties	
Electrical isolation	Channel isolated from bus Channel not isolated from channel
Operating conditions	
Mounting orientation	
Horizontal	Yes
Vertical	Yes
Installation elevation above sea level	
0 to 2000 m	No limitation
>2000 m	Reduction of ambient temperature by 0.5°C per 100 m
Degree of protection per EN 60529	IP20
Ambient conditions	
Temperature	
Operation	
Horizontal mounting orientation	-25 to 60°C
Vertical mounting orientation	-25 to 50°C
Derating	-
Storage	-40 to 85°C
Transport	-40 to 85°C
Relative humidity	
Operation	5 to 95%, non-condensing
Storage	5 to 95%, non-condensing
Transport	5 to 95%, non-condensing
Mechanical properties	
Note	Order 1x terminal block X20TB1F separately. Order 1x bus module X20BM11 separately.
Pitch	12.5$^{+0.2}$ mm

Table 2: X20AI8221 - Technical data

1)   To reduce power dissipation, B&R recommends bridging unused inputs on the terminal.
2)   Based on the current measured value.
3)   Based on the 20 V measurement range.

2.2 LED status indicators

For a description of the various operating modes, see section "Additional information - Diagnostic LEDs" in the X20 system user's manual.

Figure	LED	Color	Status	Description
	S	Green	Off	No power to module
			Single flash	UNLINK mode
			Double flash	BOOT mode (during firmware update)[1]
			Blinking quickly	SYNC mode
			Blinking slowly	PREOPERATIONAL mode
			On	RUN mode
		Red	Off	No power to module or everything OK
			On	Error or reset status

	1 - 8	Green	Off	Indicates one of the following cases:
				   • No power to module
				   • Open line

			Single flash	Input signal overflow or underflow
			On	Analog/digital converter running, value OK

1)   Depending on the configuration, a firmware update can take up to several minutes.

2.3 Pinout

AI + 1 U	AI− 1 U
AI + 2 U	AI− 2 U
AI + 3 U	AI− 3 U
AI + 4 U	AI− 4 U
AI + 5 U	AI− 5 U
AI + 6 U	AI− 6 U
AI + 7 U	AI− 7 U
AI + 8 U	AI− 8 U
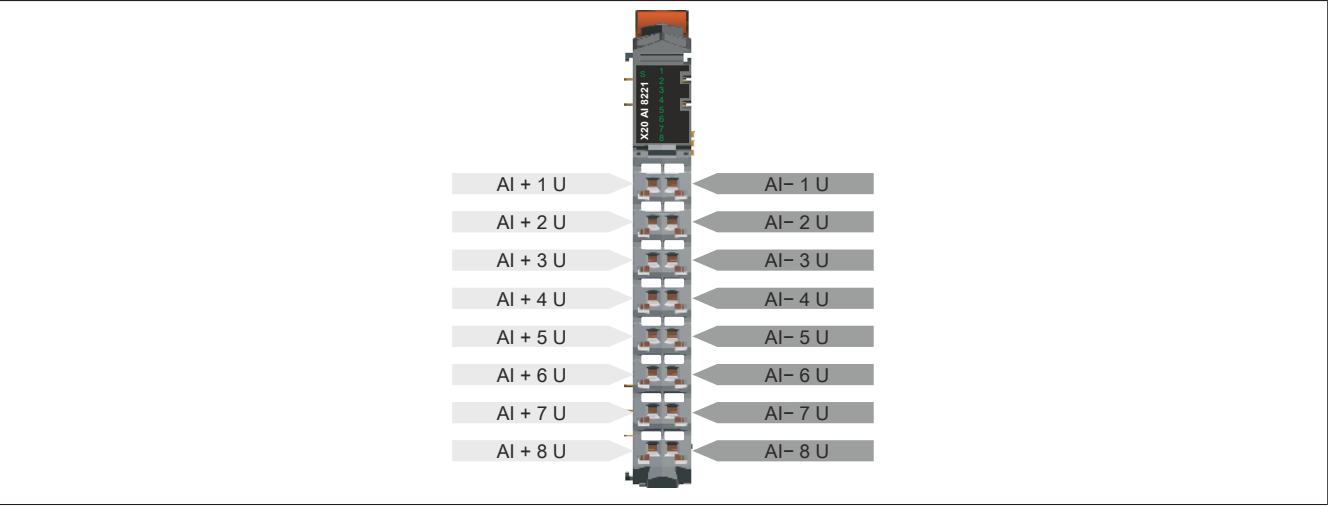
2.4 Connection example
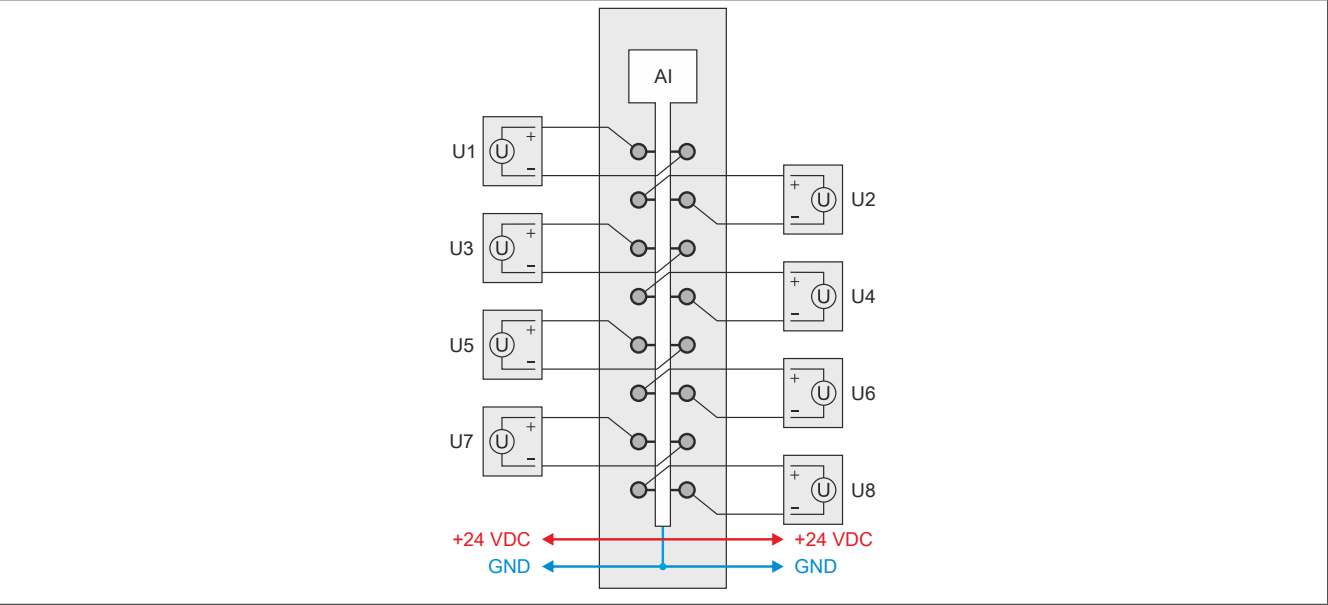
2.5 Input circuit diagram
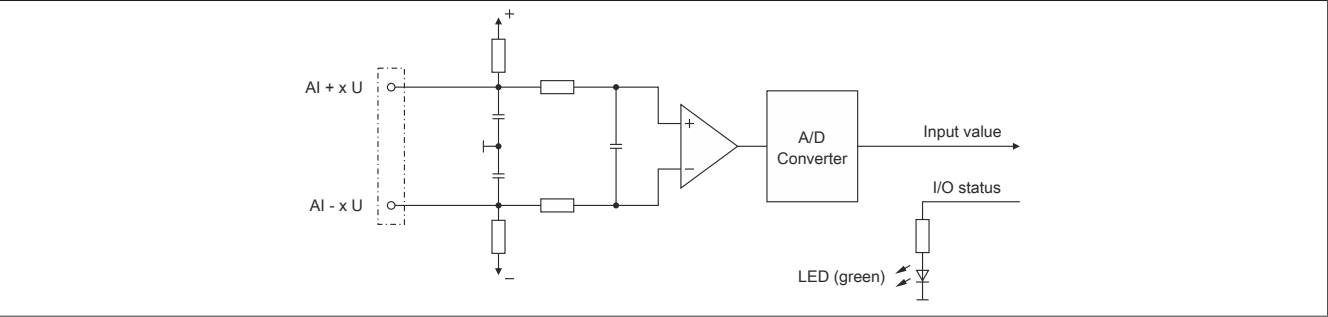
3 Function description

3.1 Input filter

This module is equipped with a configurable input filter.

> **Information:**
>
> **The filter sampling time is fixed at 1 ms and is acyclic to the X2X cycle.**

> **Information:**
>
> **The register is described in "Configuring the input filter" on page 13.**

3.1.1 Input ramp limiting

Input ramp limiting can only be performed in conjunction with filtering. Input ramp limiting is performed before filtering.

The difference of the input value change is checked for exceeding the specified limit. In the event of overshoot, the tracked input value is equal to the old value ± the limit value.

Configurable limit values:

Value	Limit value
0	The input value is used without limitation.
1	0x3FFF = 16383
2	0x1FFF = 8191
3	0x0FFF = 4095
4	0x07FF = 2047
5	0x03FF = 1023
6	0x01FF = 511
7	0x00FF = 255

Function description

Input ramp limiting is well suited for suppressing disturbances (spikes). The following examples show the functionality of input ramp limiting based on an input step and a disturbance.

Example 1

The input value jumps from 8000 to 17000. The diagram shows the tracked input value with the following settings:

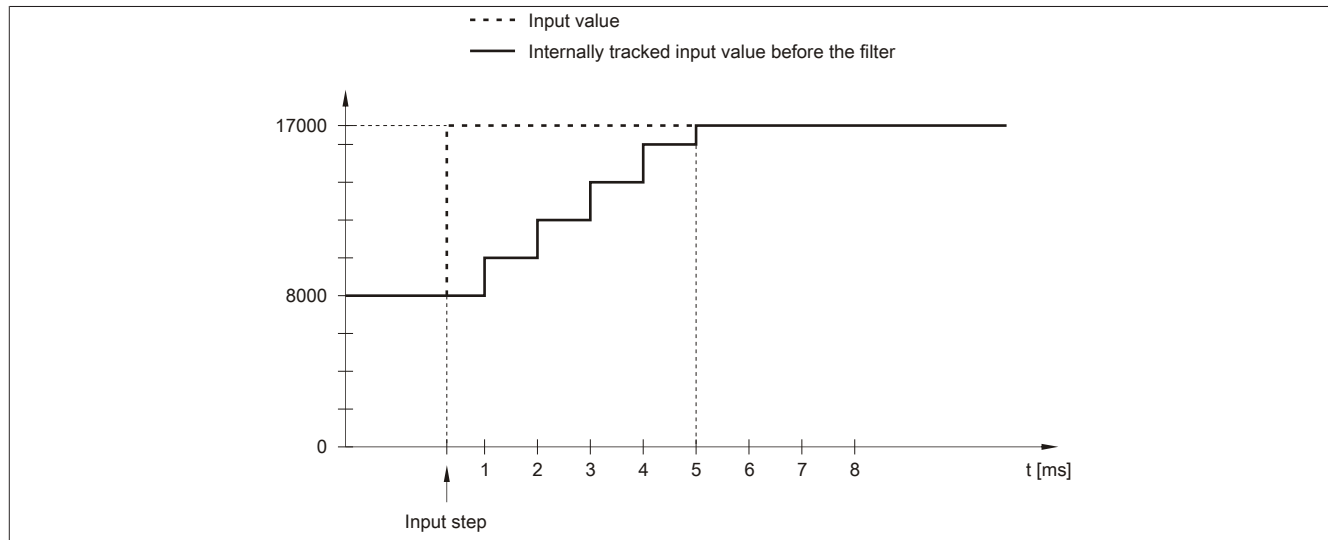Input ramp limiting = 4 = 0x07FF = 2047

Filter level = 2

Figure 1: Tracked input value for input step

Example 2

A disturbance interferes with the input value. The diagram shows the tracked input value with the following settings:

Input ramp limiting = 4 = 0x07FF = 2047
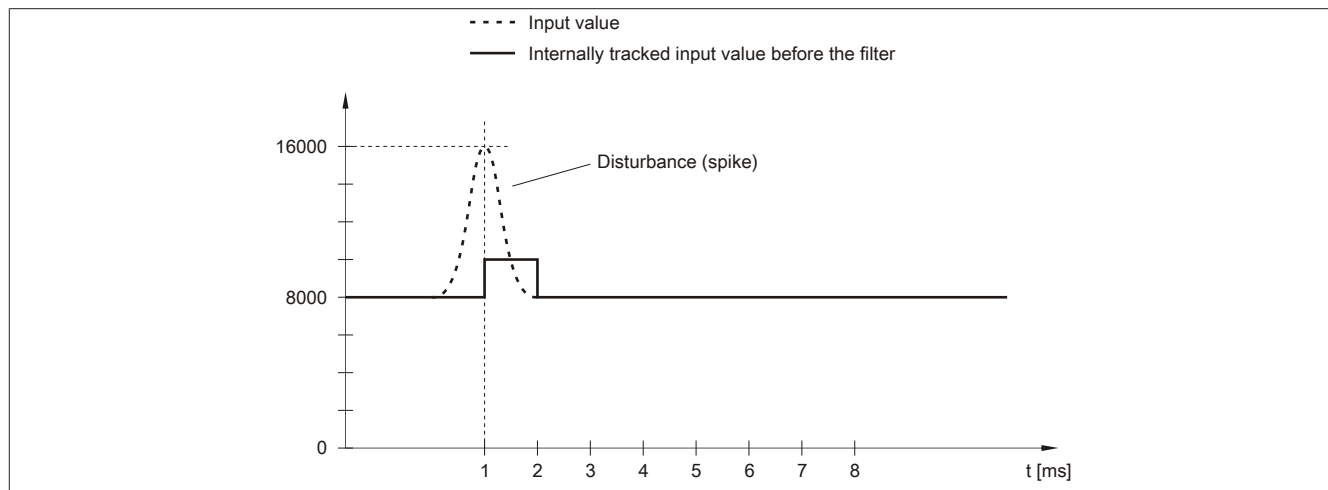
Filter level = 2

Figure 2: Tracked input value for disturbance

3.1.2 Filter level

A filter can be defined to prevent large input steps. This filter is used to bring the input value closer to the actual analog value over a period of several milliseconds.
Filtering takes place after any input ramp limiting has been carried out.

Formula for calculating the input value:

$$\text{Value}_{New} = \text{Value}_{Old} - \frac{\text{Value}_{Old}}{\text{Filter level}} + \frac{\text{Input value}}{\text{Filter level}}$$

Adjustable filter levels:

Value	Filter level
0	Filter switched off
1	Filter level 2
2	Filter level 4
3	Filter level 8
4	Filter level 16
5	Filter level 32
6	Filter level 64
7	Filter level 128

The following examples show the functionality of the filter based on an input step and a disturbance.

Example 1

The input value jumps from 8000 to 16000. The diagram shows the calculated value with the following settings:

Input ramp limiting = 0
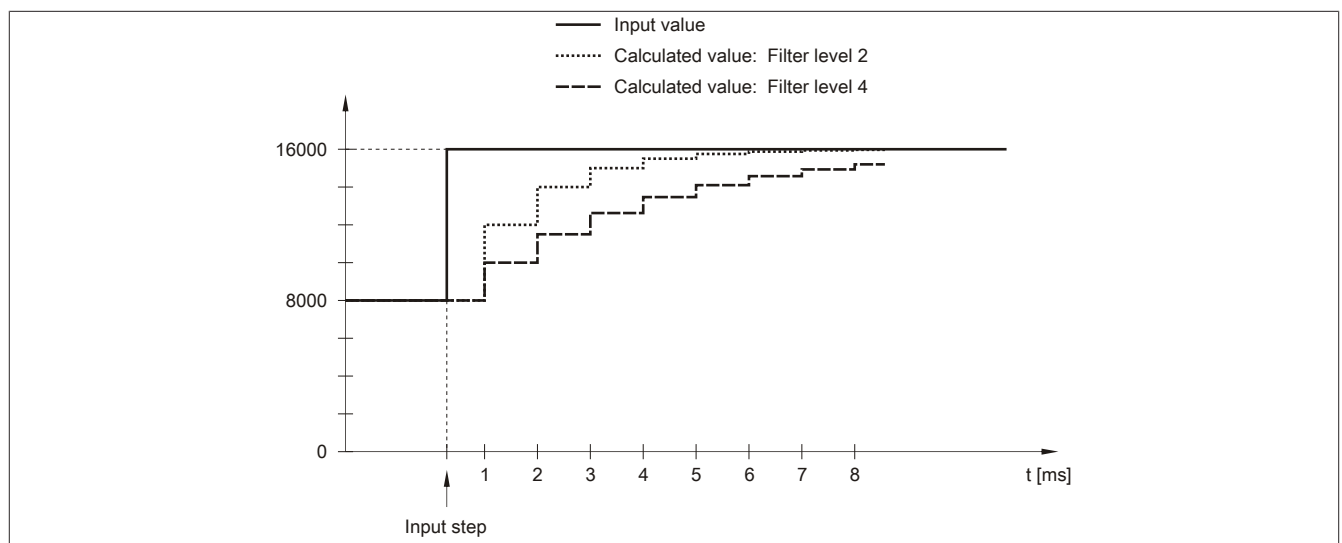
Filter level = 2 or 4

Figure 3: Calculated value during input step

Example 2

A disturbance interferes with the input value. The diagram shows the calculated value with the following settings:

Input ramp limiting = 0

Filter level = 2 or 4
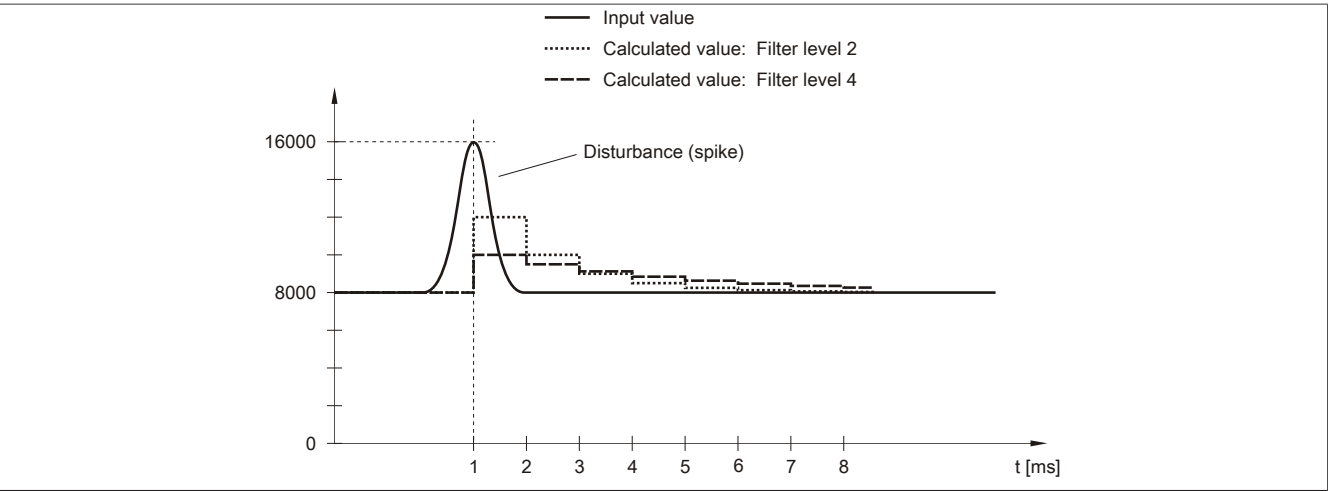
Figure 4: Calculated value during disturbance

3.2 Monitoring the input signal

The input signal is monitored against the upper and lower limit values as well as for open circuit.

Limit value (default)	Voltage signal ±10 V	
Upper maximum limit value	+10 V	+32767 (0x7FFF)
Lower minimum limit value	-10 V	-32767 (0x8001)

Limiting the analog value

In addition to the status information, the analog value is fixed to the values listed below by default in an error state. The analog value is limited to the new values if the limit values were changed.

Error state	Digital value on error (default values)
Open circuit	+32767 (0x7FFF)
Upper limit value overshot	+32767 (0x7FFF)
Lower limit value undershot	-32767 (0x8001)

> **ⓘ Information:**
>
> **The register is described in "Input status" on page 14.**

4 Commissioning

4.1 Using the module on the bus controller

Function model 254 "Bus controller" is used by default only by non-configurable bus controllers. All other bus controllers can use other registers and functions depending on the fieldbus used.

For detailed information, see section "Additional information - Using I/O modules on the bus controller" in the X20 user's manual (version 3.50 or later).

4.1.1 CAN I/O bus controller

The module occupies 2 analog logical slots on CAN I/O.

5 Register description

5.1 General data points

In addition to the registers described in the register description, the module has additional general data points. These are not module-specific but contain general information such as serial number and hardware variant.

General data points are described in section "Additional information - General data points" in the X20 System user's manual.

5.2 Function model 0 - Standard

Register	Name	Data type	Read		Write	
			Cyclic	**Non-cyclic**	**Cyclic**	**Non-cyclic**
Analog signal - Configuration						
16	ConfigOutput01 (Input filter)	USINT				●
20	ConfigOutput03 (Lower limit value)	INT				●
22	ConfigOutput04 (Upper limit value)	INT				●
Analog signal - Communication						
Index * 2 - 2	AnalogInput0N (Index N = 1 to 8)	INT	●			
30	StatusInput01	USINT	●			
31	StatusInput02	USINT	●			

5.3 Function model 254 - Bus controller

Register	Offset[1]	Name	Data type	Read		Write	
				Cyclic	**Non-cyclic**	**Cyclic**	**Non-cyclic**
Analog signal - Configuration							
16	-	ConfigOutput01 (Input filter)	USINT				●
20	-	ConfigOutput03 (Lower limit value)	INT				●
22	-	ConfigOutput04 (Upper limit value)	INT				●
Analog signal - Communication							
Index * 2 - 2	Index * 2 - 2	AnalogInput0N (Index N = 1 to 8)	INT	●			
30	-	StatusInput01	USINT		●		
31	-	StatusInput02	USINT		●		

1)   The offset specifies the position of the register within the CAN object.

5.4 Analog signal - Configuration

5.4.1 Configuring the input filter

Name:
ConfigOutput01

This register is used to define the filter level and input ramp limitation of the input filter.

Data type	Values	Bus controller default setting
USINT	See the bit structure.	0

Bit structure:

Bit	Description	Value	Information
0 - 2	Defines the filter level	000	Filter disabled (bus controller default setting)
		001	Filter level 2
		010	Filter level 4
		011	Filter level 8
		100	Filter level 16
		101	Filter level 32
		110	Filter level 64
		111	Filter level 128
3	Reserved	0	
4 - 6	Defines the input ramp limitation	000	The input value is applied without limitation (bus controller default setting)
		001	Limit value = 0x3FFF (16383)
		010	Limit value = 0x1FFF (8191)
		011	Limit value = 0x0FFF (4095)
		100	Limit value = 0x07FF (2047)
		101	Limit value = 0x03FF (1023)
		110	Limit value = 0x01FF (511)
		111	Limit value = 0x00FF (255)
7 - 15	Reserved	0	

5.4.2 Lower limit value

Name:
ConfigOutput03

The lower limit value for analog values can be set in this register. If the analog value undershoots the limit value, it is frozen at this value and the corresponding error state bit is set.

Data type	Values	Information
INT	-32767 to 32767	Bus controller default setting: -32767

> **Information:**
>
> **The default value of -32767 corresponds to the minimum default value of -10 VDC.**
>
> **It is important to note that this setting applies to all channels!**

5.4.3 Upper limit value

Name:
ConfigOutput04

The upper limit value for analog values can be set in this register. If the analog value overshoots the limit value, it is frozen at this value and the corresponding error state bit is set.

Data type	Values	Information
INT	-32767 to 32767	Bus controller default setting: 32767

> **Information:**
>
> **Default value 32767 corresponds to the maximum default value at +10 VDC.**
>
> **It is important to note that this setting applies to all channels!**

5.5 Analog signal - Communication

5.5.1 Analog inputs

Input signals are converted asynchronously in a 1 ms interval.

5.5.2 Analog input values

Name:
AnalogInput01 to AnalogInput08

The analog input values are mapped to this register.

Data type	Value	Input signal:
INT	-32,768 to 32,767	Voltage signal -10 to 10 VDC

5.5.3 Input status

Name:
StatusInput01 to StatusInput02

The module inputs are monitored in this register. A change in the monitoring status is actively issued as an error message and, in the event of an error, the analog value is fixed at defined values. For details, see "Monitoring the input signal" on page 10.

Data type	Values
USINT	See the bit structure.

Bit structure:

StatusInput01 monitors Channels 1 to 4
StatusInput02 monitors Channels 5 to 8

Bit	Description	Value	Information
0 - 1	Channel 1 or 5	00	No error
		01	Lower limit value exceeded
		10	Upper limit value exceeded
		11	Open line
...		...	
6 - 7	Channel 4 or 8	00	No error
		01	Lower limit value exceeded
		10	Upper limit value exceeded
		11	Open line

5.6 Minimum cycle time

The minimum cycle time specifies how far the bus cycle can be reduced without communication errors occurring. It is important to note that very fast cycles reduce the idle time available for handling monitoring, diagnostics and acyclic commands.

Minimum cycle time
150 µs

5.7 Minimum I/O update time

The minimum I/O update time specifies how far the bus cycle can be reduced so that an I/O update is performed in each cycle.

Minimum I/O update time
1 ms